

FIG. 2

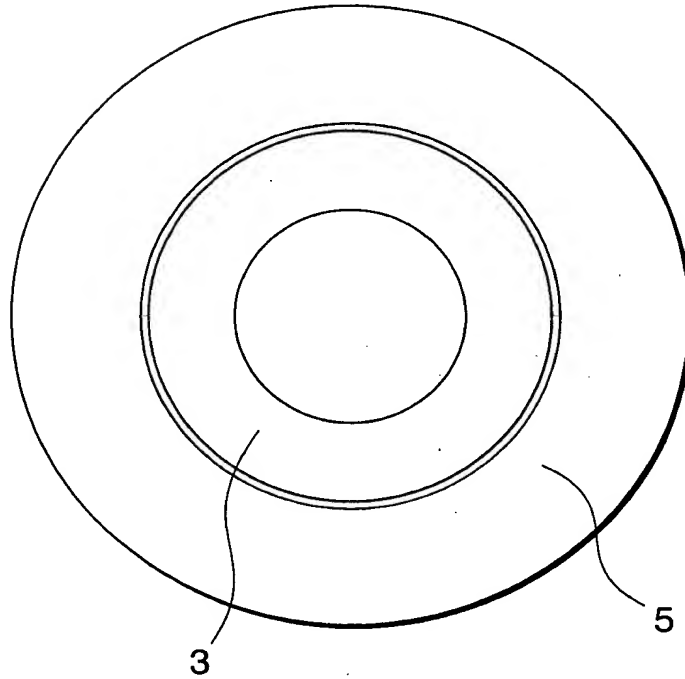


FIG. 3

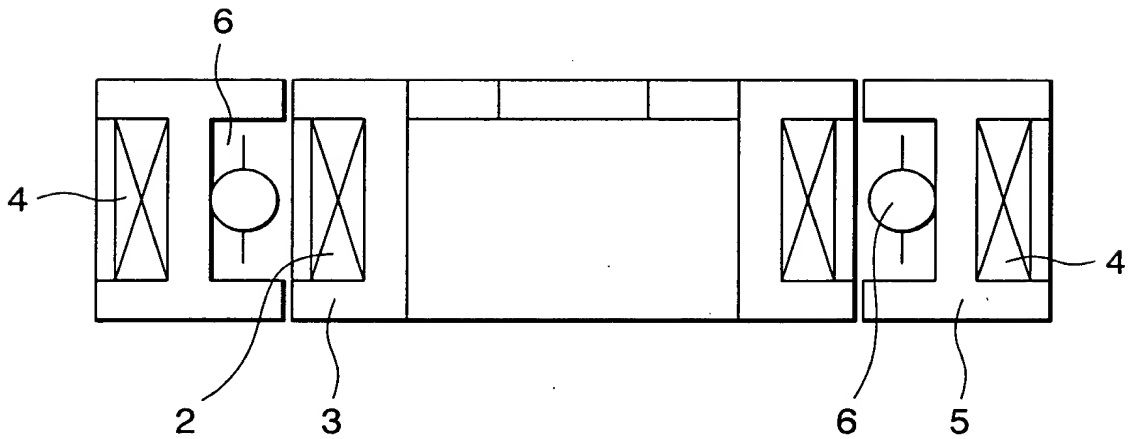


FIG. 4

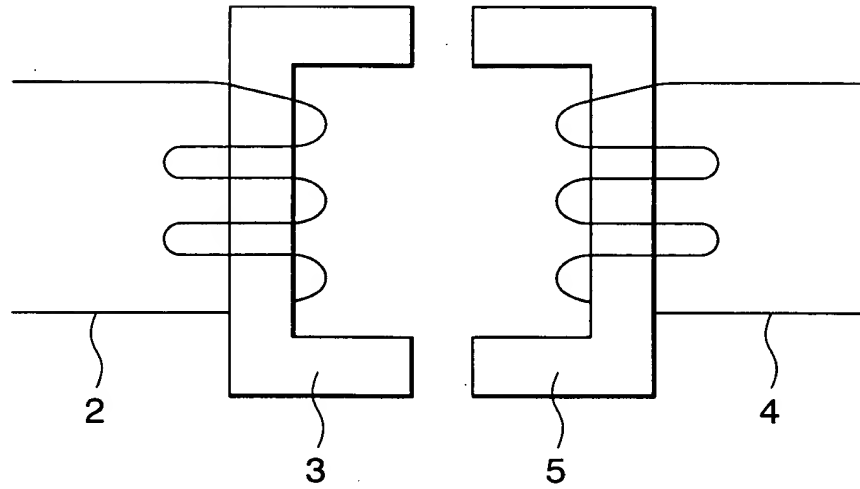


FIG. 5

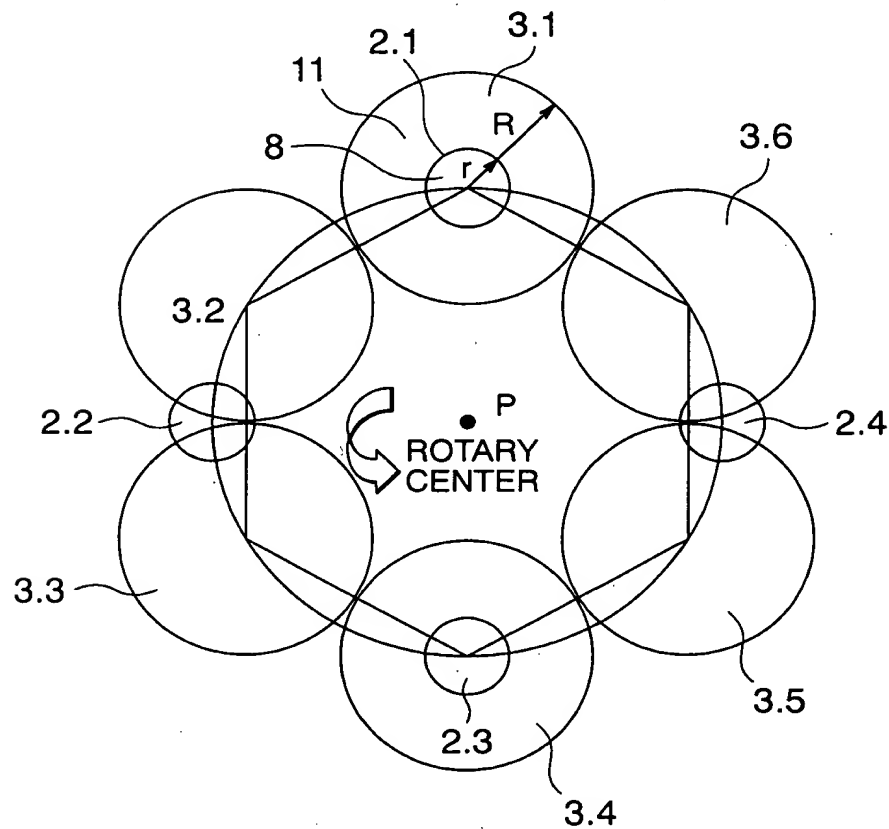


FIG. 5

FIG. 6

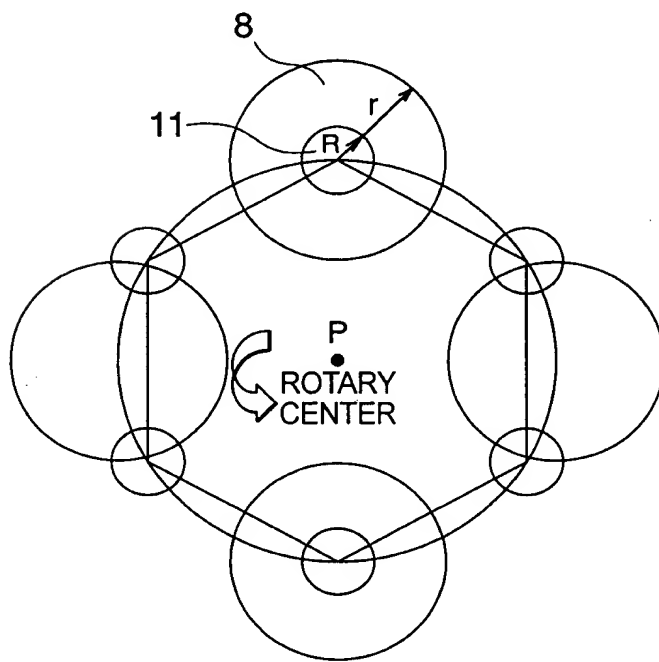
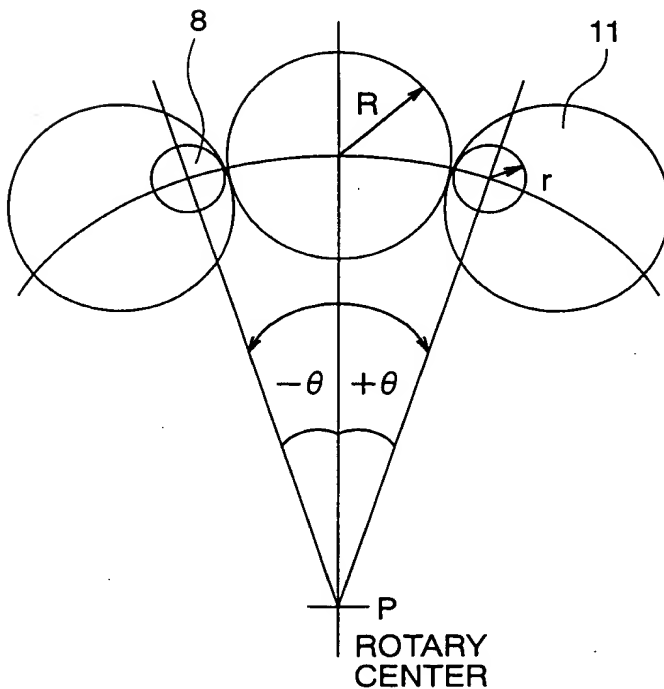


FIG. 7



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FIG. 8

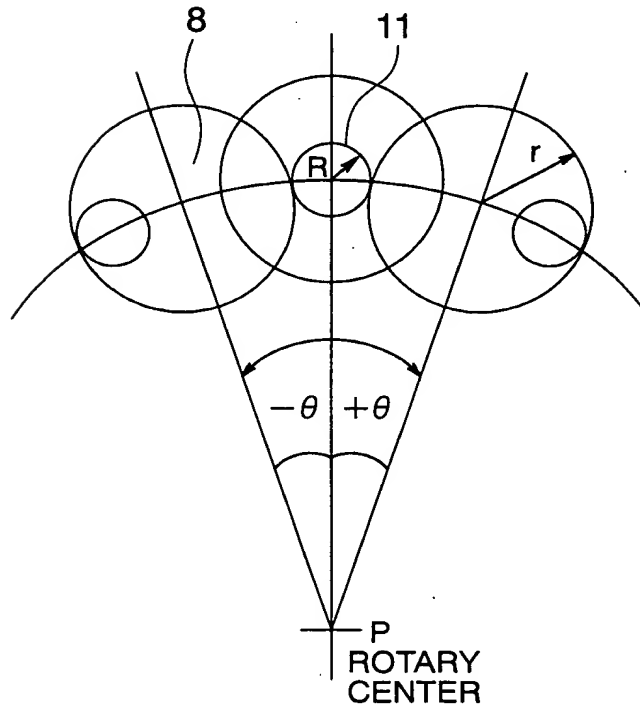
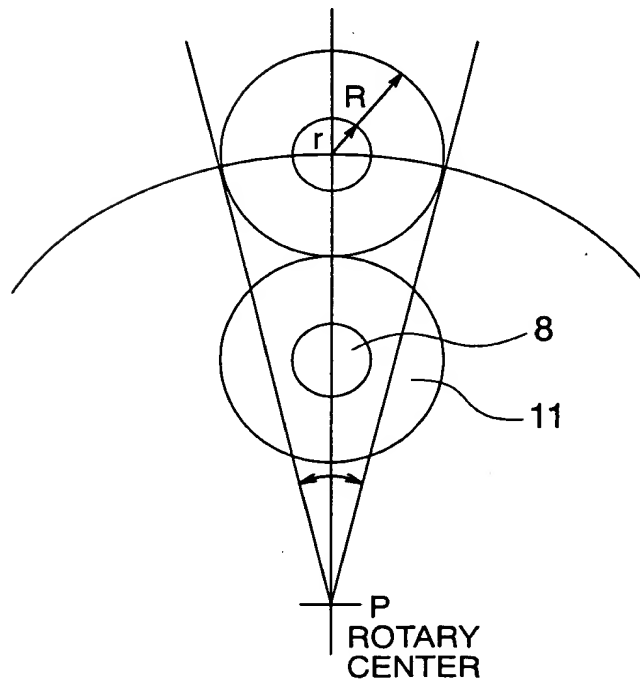


FIG. 9



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FIG. 10

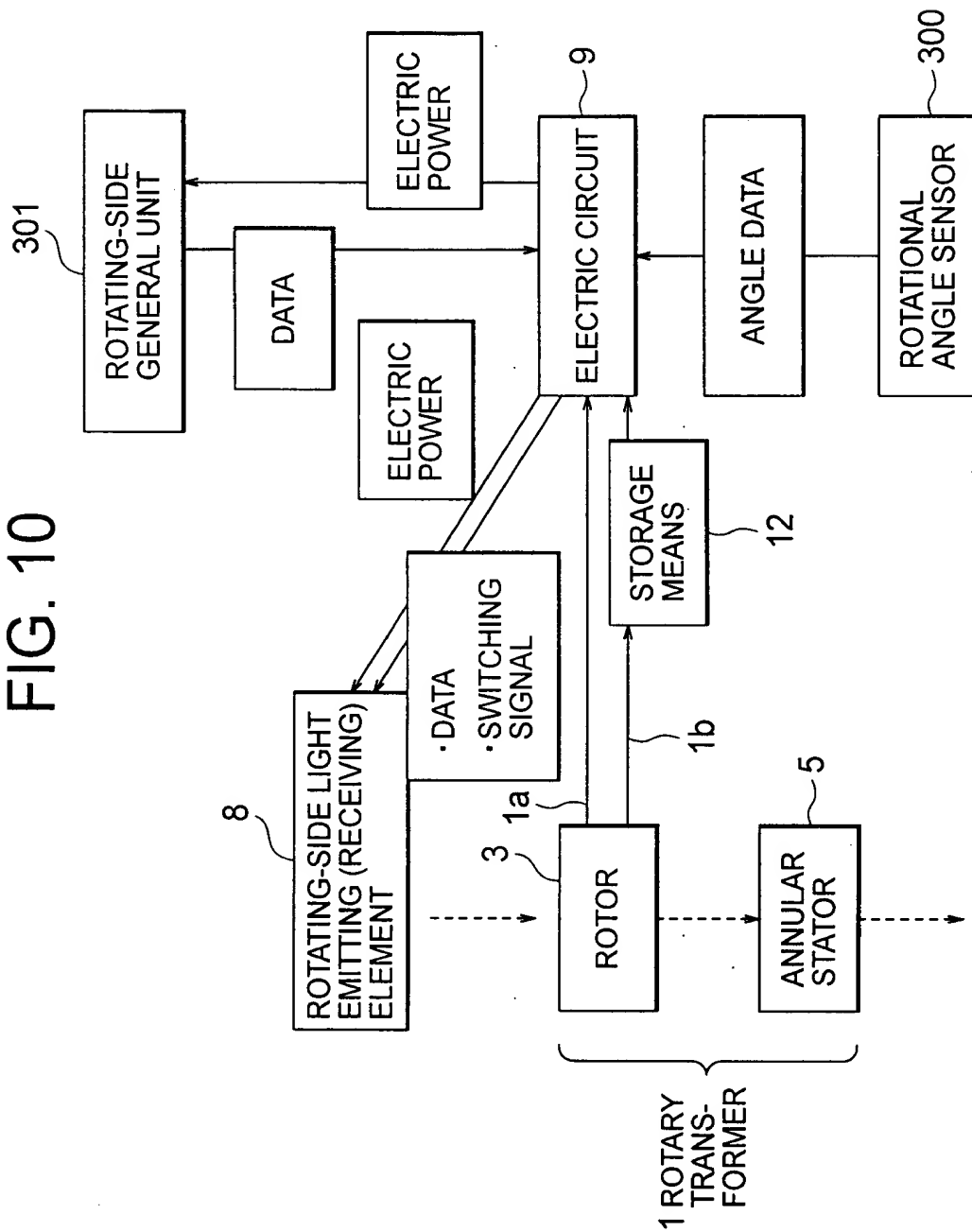
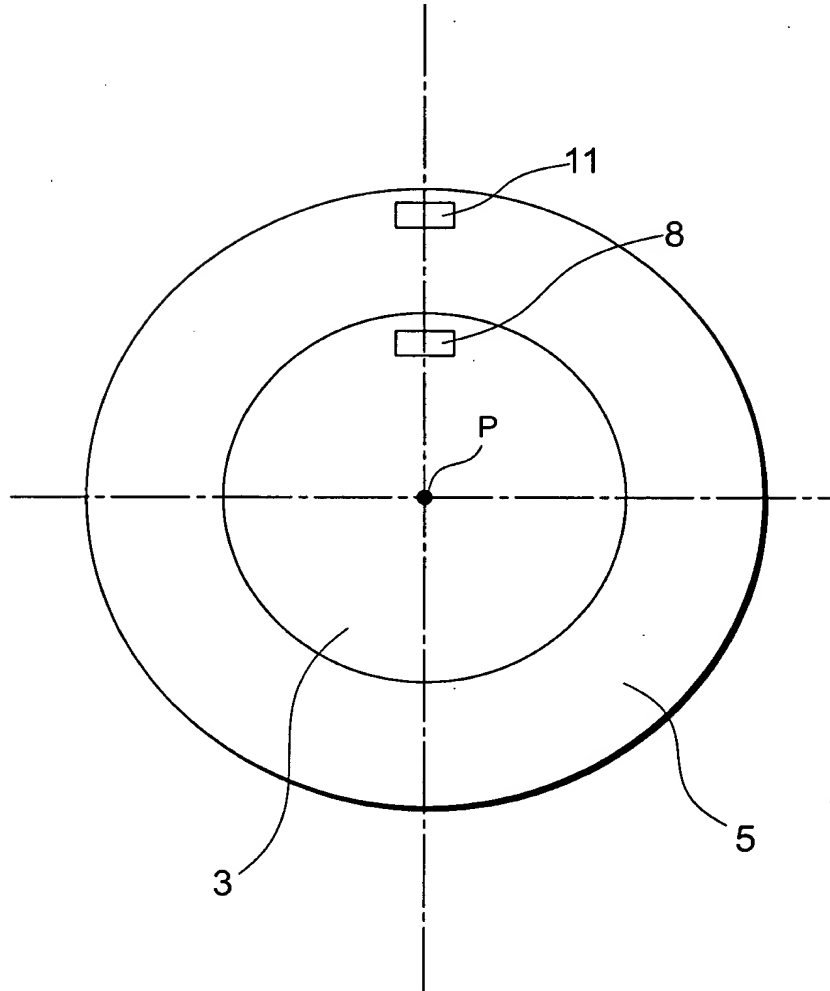


Figure 1 is a schematic diagram of a display device 200, showing a top view of a first substrate 101 and a second substrate 103. The first substrate 101 includes a first display area 100, a first gate line 110, a first data line 120, and a first pixel area 130. The second substrate 103 includes a second display area 102, a second gate line 111, and a second pixel area 103. A gap D is shown between the two substrates.

FIG. 12



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